

(b) Section 201.6 of this chapter on the payment and refund of Copyright Office fees shall apply to mask work fees.

[50 FR 26719, June 28, 1985, as amended at 56 FR 59886, Nov. 26, 1991; 59 FR 38372, July 28, 1994; 63 FR 29139, May 28, 1998; 64 FR 29522, June 1, 1999]

**§211.4 Registration of claims of protection in mask works.**

(a) *General.* This section prescribes conditions for the registration of claims of protection in mask works pursuant to section 908 of title 17 U.S.C.

(b) *Application for registration.* (1) For purposes of registration of mask work claims, the Register of Copyrights has designated “Form MW” to be used for all applications submitted on and after January 7, 1985. Copies of the form are available free upon request to the Copyright Information Office, U.S. Copyright Office, Library of Congress, Washington, DC 20559. Applications submitted before January 7, 1985 will be dated January 7, 1985.

(2) An application for registration of a mask work claim may be submitted by the owner of the mask work, or the duly authorized agent of any such owner.

(i) The owner of a mask work includes a party that has obtained the transfer of all of the exclusive rights in the work, but does not include the transferee of less than all of the exclusive rights, or the licensee of all or less than all of these rights.

(ii) For purposes of eligibility to claim mask work protection pursuant to section 902(a)(1)(A) of 17 U.S.C., the owner of the mask work must be either the initial owner or a person who has obtained by transfer the totality of rights in the mask work under the Act.

(3) An application for registration shall be submitted on Form MW prescribed by the Register under paragraph (b)(1) of this section, and shall be accompanied by the registration fee and deposit required under 17 U.S.C. 908 and §§211.3 and 211.5 of these regulations. The application shall contain the information required by the form and its accompanying instructions, and shall include a certification. The certification shall consist of:

(i) A declaration that the applicant is authorized to submit the application and that the statements made are correct to the best of that person’s knowledge; and

(ii) The handwritten signature of the applicant, accompanied by the typed or printed name of that person.

(c) *One registration per mask work.* (1) Subject to the exception specified in paragraph (c)(2) of this section, only one registration can generally be made for the same version of a mask work fixed in an intermediate or final form of any semiconductor chip product. However, where an applicant for registration alleges that an earlier registration for the same version of the work is unauthorized and legally invalid and submits for recordation a signed affidavit, a registration may be made in the applicant’s name.

(2) Notwithstanding the general rule permitting only one registration per work, owners of mask works in final forms of semiconductor chip products that are produced by adding metal-connection layers to unpersonalized gate arrays may separately register the entire unpersonalized gate array and the custom metallization layers. Applicants seeking to register separately entire unpersonalized gate arrays or custom metallization layers should make the nature of their claim clear at Space 8 of application Form MW. For these purposes, an “unpersonalized gate array” is an intermediate form chip product that includes a plurality of circuit elements that are adaptable to be personalized into a plurality of different final form chip products, in which some of the circuit elements are, or will be, connected as gates.

(d) *Registration as a single work.* Subject to the exception specified in paragraph (c)(2) of this section, for purposes of registration on a single application and upon payment of a single fee, the following shall be considered a single work.

(1) In the case of a mask work that has not been commercially exploited: All original mask work elements fixed in a particular form of a semiconductor chip product at the time an application for registration is filed and in which the owner or owners of the mask work is or are the same; and

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(2) In the case of a mask work that has been commercially exploited. All original mask work elements fixed in a semiconductor chip product at the time that product was first commercially exploited and in which the owner or owners of the mask is or are the same.

(e) *Registration in most complete form.* Owners seeking registration of a mask work contribution must submit the entire original mask work contribution in its most complete form as fixed in a semiconductor chip product. The most complete form means the stage of the manufacturing process which is closest to completion. In cases where the owner is unable to register on the basis of the most complete form because he or she lacks control over the most complete form, an averment of this fact must be made at Space 2 of Form MW. Where such an averment is made, the owner may register on the basis of the most complete form in his or her possession. For applicants seeking to register an unpersonalized gate array or custom metallization layers under paragraph (c)(2) of this section, the most complete form is the entire chip on which the unpersonalized gate array or custom metallization layers reside(s), and registration covers those elements of the chip in which work protection is asserted.

(f) *Corrections and amplifications of prior registration.* Except for errors or omissions made by the Copyright Office, no corrections or amplifications can be made to the information contained in the record of a completed registration after the effective date of the registration. A document purporting to correct or amplify the information in a completed registration may be recorded in the Copyright Office for whatever effect a court of competent jurisdiction may later give to it, if the document is signed by the owner of the mask work, as identified in the registration record, or by a duly authorized agent of the owner.

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## 37 CFR Ch. II (7-1-10 Edition)

### §211.5 Deposit of identifying material.

(a) *General.* This section prescribes rules pertaining to the deposit of identifying material for registration of a claim of protection in a mask work under section 908 of title 17 U.S.C.

(b) *Nature of required deposit.* Subject to the provisions of paragraph (c) of this section, the deposit of identifying material to accompany an application for registration of a mask work claim under §211.4 of these regulations shall consist of:

(1) In the case of a commercially exploited mask work, four reproductions of the mask work fixed in the form of the semiconductor chip product in which it was first commercially exploited. Defective chips may be deposited under this section provided that the mask work contribution would be revealed in reverse dissection of the chips. The four reproductions shall be accompanied by a visually perceptible representation of each layer of the mask work consisting of:

(i) Sets of plastic color overlay sheets;

(ii) Drawings or plots in composite form on a single sheet or on separate sheets; or

(iii) A photograph of each layer of the work fixed in a semiconductor chip product.

The visually perceptible representation of a mask work deposited under this section shall be reproduced on material which can be readily stored in an 8½×11 inch format, and shall be reproduced at a magnification sufficient to reveal the basic circuitry design of the mask work and which shall in all cases be at least 20 times magnification.

(2) In the case of a mask work that has not been commercially exploited, one of the following:

(i) Where the mask work contribution in which registration is sought represents twenty percent or more of the area of the intended final form, a visually perceptible representation of the work in accordance with paragraph (b)(1) (i) or (ii) of this section. In addition to the deposit of visually perceptible representations of the work, an applicant may, at his or her option, deposit four reproductions in the most complete form of the mask work as fixed in a semiconductor product.